

G83/2-1 Engineering Recommendation

Type Approval and manufacturer/supplier declaration of compliance with the requirements of Engineering Recommendation G83/2-1.			
SSEG Type reference number		Growatt 3000S2018	
SSEG Type		Growatt 750-S , Growatt 1000-S , Growatt 1500-S , Growatt 2000-S, Growatt 2500-S, Growatt 3000-S	
System Supplier name		Growatt New Energy Technology Co., Ltd.	
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Tel	+86 755 2951 5888	Fax	+86 755 2747 2131
E-mail	info@ginverter.com	Web site	www.ginverter.com
Maximum rated capacity	Connection Option		
	3.0	kW single phase, single, split or three phase system	
	N/A	KW three phase	
	N/A	kW two phase in three phase system	
	N/A	kW two phase split phase system	
<p>SSEG manufacturer/supplier declaration.</p> <p>I certify on behalf of the company named above as a manufacturer/supplier of Small Scale Embedded Generators, that all products manufactured/supplied by the company with the above SSEG Type reference number will be manufactured and tested to ensure that they perform as stated in this Type Verification Test Report, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of G83/2-1.</p>			
Singed	<i>James Wang</i>	On behalf of	Growatt New Energy Technology Co., Ltd.
<p>Note that testing can be done by the manufacturer of an individual component, by an external test house, or by the supplier of the complete system, or any combination of them as appropriate.</p> <p>Where parts of the testing are carried out by persons or organisations other than the supplier then the supplier shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.</p> <p>The family product model is made by the following products: Growatt 750-S , Growatt 1000-S , Growatt 1500-S , Growatt 2000-S, Growatt 2500-S, Growatt 3000-S</p> <p>The model Growatt 3000-S is as the representative test models in this report.</p>			

G83/2-1 Appendix 4 Type Verification Test Report

Power Quality. Harmonics.						
The requirement is specified in section 5.4.1, test procedure in Annex A or B 1.4.1						
SSEG rating per phase (rpp)			3.0	kW	NV=MV*3.68/rpp	
Harmonic	At 45-55% of rated output		100% of rated output			
	Measured Value (MV) in Amps	Normalised Value (NV) in Amps	Measured Value (MV) in Amps	Normalised Value (NV) in Amps	Limit in BS EN 61000-3-2 in Amps	Higher limit for odd harmonics 21 and above
2	0.065	0.080	0.11	0.135	1.080	
3	0.187	0.229	0.21	0.258	2.300	
4	0.027	0.033	0.04	0.049	0.430	
5	0.113	0.139	0.131	0.161	1.140	
6	0.019	0.023	0.029	0.036	0.300	
7	0.069	0.085	0.079	0.097	0.770	
8	0.007	0.009	0.009	0.011	0.230	
9	0.045	0.055	0.059	0.072	0.400	
10	0.005	0.006	0.019	0.023	0.184	
11	0.027	0.033	0.049	0.060	0.330	
12	0.007	0.009	0.009	0.011	0.153	
13	0.007	0.009	0.051	0.063	0.210	
14	0.007	0.009	0.021	0.026	0.131	
15	0.008	0.010	0.031	0.038	0.150	
16	0.008	0.010	0.021	0.026	0.115	
17	0.017	0.021	0.049	0.060	0.132	
18	0.007	0.009	0.019	0.023	0.102	
19	0.027	0.033	0.049	0.060	0.118	
20	0.007	0.009	0.019	0.023	0.92	
21	0.037	0.045	0.069	0.085	0.107	0.160
22	0.017	0.021	0.019	0.023	0.084	
23	0.027	0.033	0.059	0.072	0.098	0.147
24	0.007	0.009	0.029	0.036	0.077	
25	0.037	0.045	0.059	0.072	0.090	0.135
26	0.007	0.009	0.009	0.011	0.071	
27	0.027	0.033	0.037	0.045	0.083	0.124
28	0.007	0.009	0.007	0.009	0.066	
29	0.038	0.047	0.049	0.060	0.078	0.117
30	0.008	0.010	0.009	0.011	0.061	
31	0.018	0.022	0.029	0.036	0.073	0.109

32	0.007	0.009	0.017	0.021	0.058	
33	0.017	0.021	0.027	0.033	0.068	0.102
34	0.007	0.009	0.019	0.023	0.054	
35	0.017	0.021	0.029	0.036	0.064	0.096
36	0.007	0.009	0.009	0.011	0.051	
37	0.007	0.009	0.019	0.023	0.061	0.091
38	0.007	0.009	0.009	0.011	0.048	
39	0.007	0.009	0.019	0.023	0.058	0.087
40	0.008	0.010	0.011	0.013	0.046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

Power Quality. Voltage fluctuations and Flicker.

The requirement is specified in section 5.4.2, test procedure in Annex A or B 1.4.3

	Starting			Stopping			Running	
	d _{max}	d _c	d(t)	d _{max}	d _c	d(t)	P _{st}	P _{It} 2 hours
Measured Values	1.07	0.03	0	1.07	0.24	0	0.20	0.14
Normalised to standard impedance and 3.68kW for multiple units	1.08	0.03	0	1.08	0.25	0	0.21	0.15
Limits set under BS EN 61000-3-2	4%	3.3%	3.3% <small>500ms</small>	4%	3.3%	3.3% <small>500ms</small>	1.0	0.65
Test start date	20,June,2018			Test end date			20,June,2018	
Test location	Growatt R&D Test Lab							

Power Quality. DC injection.

The requirement is specified in section 5.5, test procedure in Annex A or B 1.4.4

Test power level	10%	55%	100%
Recorded value	15.5mA	13.1mA	15.7mA
as % of rated AC current	0.12%	0.1%	0.12%
Limit	0.25%	0.25%	0.25%

Power Quality. Power factor.

The requirement is specified in section 5.6, test procedure in Annex A or B 1.4.2

	216.2V	230V	253V	Measured at three voltage levels and at full output.
Measured value	0.998	0.998	0.997	
Limit	>0.95	>0.95	>0.95	Voltage to be maintained within ±1.5% of the stated level during the test.

Protection. Frequency tests.						
The requirement is specified in section 5.3.1, test procedure in Annex A or B 1.3.3						
Function	Setting		Trip test		"No trip tests"	
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5Hz	20s	47.51Hz	20.04s	47.7Hz/25s	N o Trip
U/F stage 2	47Hz	0.5s	47.01Hz	0.52s	47.2Hz/19.98s	N o Trip
					46.8Hz/0.48s	N o Trip
O/F stage 1	51.5Hz	90s	51.49 Hz	90.03s	51.3Hz/95s	N o Trip
O/F stage 2	52Hz	0.5s	52.0 Hz	0.54s	51.8Hz/89.98s	N o Trip
					52.2Hz/0.48s	N o Trip

Protection. Voltage tests.						
The requirement is specified in section 5.3.1, test procedure in Annex A or B 1.3.2						
Function	Setting		Trip test		"No trip tests"	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V stage 1	200.1V	2.5s	200.55V	2.522s	204.1V/3.5s	N o Trip
U/V stage 2	184V	0.5s	184.5V	0.583s	188V/2.48s	N o Trip
					180V/0.48s	N o Trip
O/V stage 1	262.2V	1.0s	262.78V	1.052s	258.2V/2.0s	N o Trip
O/V stage 2	273.7V	0.5s	274V	0.574s	269.7V/0.98s	N o Trip
					277.7V/0.48s	N o Trip
<p>Note for Voltage tests the Voltage required to trip is the setting $\pm 3.45V$. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting $\pm 4V$ and for the relevant times as shown in the table above to ensure that the protection will not trip in error.</p>						

Protection. Loss of Mains test.							
The requirement is specified in section 5.3.2, test procedure in Annex A or B 1.3.4							
Test Power	10%	55%	100%	10%	55%	100%	
Balancing load on islanded network	95% of SSEG output	95% of SSEG output	95% of SSEG output	105% of SSEG output	105% of SSEG output	105% of SSEG output	
Trip time. Limit is 0.5 seconds	0.29s	0.34s	0.31s	0.32s	0.31s	0.30s	
For Multi phase SSEGs confirm that the device shuts down correctly after the removal of a single fuse as well as operation of all phases.							
Test Power	10%	55%	100%	10%	55%	100%	
Balancing load on islanded network	95% of SSEG	95% of SSEG	95% of SSEG	105% of SSEG	105% of SSEG	105% of SSEG	

	output	output	output	output	output	output
Trip time. Ph1 fuse removed	/	/	/	/	/	/
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of SSEG output	95% of SSEG output	95% of SSEG output	105% of SSEG output	105% of SSEG output	105% of SSEG output
Trip time. Ph2 fuse removed	/	/	/	/	/	/
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of SSEG output	95% of SSEG output	95% of SSEG output	105% of SSEG output	105% of SSEG output	105% of SSEG output
Trip time. Ph3 fuse removed	/	/	/	/	/	/
Note for technologies which have a substantial shut down time this can be added to the 0.5 seconds in establishing that the trip occurred in less than 0.5s. Maximum shut down time could therefore be up to 1.0 seconds for these technologies.						
Indicate additional shut down time included in above results.					ms	
Note as an alternative, inverters can be tested to BS EN 62116. The following sub set of tests should be recorded in the following table.						
Test power and imbalance	33% -5 Q test 22	66% -5 Q test 12	100% -5 P test 5	33% +5 Q test 31	66% +5 Q test 21	100% +5 P test 10
Trip time. Limit is 0.5s	0.31s	0.31s	0.30s	0.34s	0.29s	0.29s

Protection. Frequency change, Vector Shift Stability test.

The requirement is specified in section 5.3.3, test procedure in Annex A or B 1.3.6

	Start Frequency	Change	End Frequency	Confirm no trip
Positive Vector Shift	49.5Hz	+50 degrees		N o Trip
Negative Vector Shift	50.5Hz	-50 degrees		N o Trip

Protection. Frequency change, RoCoF Stability test.

The requirement is specified in section 5.3.3, test procedure in Annex A or B 1.3.6

Ramp range	Test frequency ramp:	Test Duration	Confirm no trip
49.0Hz to 51.0Hz	+0.95Hzs ⁻¹	2.1s	N o Trip
51.0Hz to 49.0Hz	-0.95Hzs ⁻¹	2.1s	N o Trip

Protection. Re-connection timer.					
The requirement is specified in section 5.3.4, test procedure in Annex A or B 1.3.5					
Test should prove that the reconnection sequence starts after a minimum delay of 20 seconds for restoration of voltage and frequency to within the stage 1 settings of table 1.					
Time delay setting	Measured delay	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1.			
60s	62s	At 266.2V	At 196.1V	At 47.4Hz	At 51.6Hz
Confirmation that the SSEG does not re-connect.		yes	yes	yes	yes

Fault level contribution.					
The requirement is specified in section 5.7, test procedure in Annex A or B 1.4.6					
For a directly coupled SSEG			For a Inverter SSEG		
Parameter	Symbol	Value	Time after fault	Volts	Amps
Peak Short Circuit current	i_p	/	20ms	81.2V	29.3A
Initial Value of aperiodic current	A	/	100ms	77.3V	22.5A
Initial symmetrical short-circuit current*	I_k	/	250ms	76.9V	16.1A
Decaying (aperiodic) component of short circuit current*	i_{DC}	/	500ms	73.5V	8.6A
Reactance/Resistance Ratio of source*	X/R	/	Time to trip	0.11	In seconds
Self-Monitoring solid state switching					YES/ or NA
The requirement is specified in section 5.3.1, No specified test requirements					NA
It has been verified that in the event of the solid state switching device failing to disconnect the SSEG, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 seconds.					NA

Additional comments