

## ENGINEERING RECOMMENDATION G83/1-1

### APPENDIX 4 TYPE VERIFICATION TEST SHEET

#### SSEG DETAILS

SSEG Type reference: S270-ACB-LN-yy, yy = blank, or UK		
SSEG Technology (as per Annex): C		
Manufacturer:	Tel: <i>+1-707-763-4784</i>	Address: <i>Enphase Energy inc. 1420 North McDowell Blvd. Petaluma, Ca 94954 USA</i>
	Fax: <i>+1-707-763-0784</i>	
Technical file reference No: <i>20171025_01</i>		
Maximum export capability (SSEG rating less parasitic load)		<i>260 W/unit</i>

#### TEST HOUSE DETAILS

Name and address of test house	<i>Enphase Energy 1 Treffers road Wigram 8042 Christchurch New Zealand</i>
Telephone number	<i>+64 3 345 5300</i>
Facsimile number	
E-mail address	<i>dkeis@enphaseenergy.com</i>

#### TEST DETAILS

Date of test	<i>October 2017</i>
Name of tester	<i>Daniel Keis</i>
Signature of tester	<i>D. Keis</i>
Test location if different from above	

## ENGINEERING RECOMMENDATION G83/1-1

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#### POWER QUALITY

Harmonic current emissions (A)								
Harmonic	2 <sup>nd</sup>	3 <sup>rd</sup>	5 <sup>th</sup>	7 <sup>th</sup>	9 <sup>th</sup>	11 <sup>th</sup>	13 <sup>th</sup>	15 <sup>th</sup> ≤ n ≤ 39 <sup>th</sup>
Limit *	1.08	2.3	1.14	0.77	0.4	0.33	0.21	0.15 x (15/n)
Test value	0.019	0.028	0.039	0.020	0.013	0.012	0.006	0.006 (max)

Note \* Maximum permissible harmonic current As per BS EN 61000-3-2 Class A.

Voltage Fluctuations and Flicker				
	Starting	Stopping	Running	
Limit *	4%	4%	P <sub>st</sub> = 1.0	P <sub>lt</sub> = 0.65
Test Value	0.25%	0.25%	0.08	0.07

Note Maximum permissible voltage fluctuation (expressed as a percentage of nominal voltage at 100% power) and flicker. As per BS EN 61000-3-3.

	DC injection			Power factor		
G83/1 Limit	20mA, tested at three power levels *			0.95 lag– 0.95 lead at three voltage levels		
Test level	10%	55%	100%	212 V	230 V	248 V
Test value #	8 nA	8 nA	8 nA	1.00	1.00	1.00

Notes \* Indicative values are shown for minimum, medium and maximum power levels.

# Insert maximum value recorded during testing

#### UNDER / OVER FREQUENCY TESTS

Parameter	Under Frequency		Over Frequency	
	Frequency	Time	Frequency	Time
G83/1 Limit	47 Hz	0.5 sec *	50.5 Hz	0.5 sec *
Actual setting	47 Hz	0.5 sec	50.5 Hz	0.5 sec
Trip value	47.04 Hz	0.40 sec	50.47 Hz	0.49 sec

#### UNDER / OVER VOLTAGE TESTS

Parameter	Under Voltage		Over Voltage	
	Voltage	Time	Voltage	Time
G83/1 Limit	207 V	1.5 sec *	264 V	1.5 sec *
Actual setting	209.05 V	1.5 sec	264 V	1.5 sec
Trip value	208.6 V	1.46 sec	263.3 V	1.46 sec

Note: \* For SSEG units that can withstand being re-energised from a source that is 180 out of phase with the SSEG output, it is permissible to extend the operating time of the interface protection to 5.0 seconds, as described in 5.3.1. Table 1.

## ENGINEERING RECOMMENDATION G83/1-1

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#### LOSS OF MAINS TEST

Method used:	Active Anti-Islanding as per VDE0126		
Output power level *	33%	55%	100%
Trip setting	0.5 s	0.5 s	0.5 s
Trip value	0.19 s	0.23 s	0.22 s

Note \* Indicative values are shown for minimum, medium and maximum power levels.

#### RECONNECTION TIMES

Reconnection Time	Under/Over voltage	Under/Over Frequency	Loss of mains
Minimum value	180 seconds	180 seconds	180 seconds
Actual Setting	180 seconds	180 seconds	180 seconds
Recorded value	182 seconds	180 seconds	181 seconds

#### FAULT LEVEL CONTRIBUTION

##### SSEG Short Circuit Test

This test should determine the value of short circuit current at the SSEG terminals as described in clause 5.7 in Engineering Recommendation G83/1.

For rotating machines and linear piston machines the test should produce a 0 – 2.0 second plot of the short circuit current as seen at the SSEG terminals.

##### SSEG Short Circuit Parameters

Parameter	Symbol	Value
Peak short-circuit current	$i_p$	7.48
Initial value of aperiodic component	$A$	5.74
Initial symmetrical short-circuit current *	$I_k$	6.36
Decaying (aperiodic) component of short-circuit current *	$i_{DC}$	0
Reactance / Resistance Ratio of source *	$X/R$	2.5

Note \* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot

## ENGINEERING RECOMMENDATION G83/1-1

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#### SELF MONITORING – SOLID STATE SWITCHING

Test	Yes / No
It has been verified that in the event of the solid state switching device failing to disconnect the SSEG, the voltage on the output side of the switching device is reduced to a value below 50 volt within 0.5 sec.	Yes

#### COMMENTS

This energy storage inverter is contained within model number:  
B270-1200-LN-I-AU00-RV0